G99/1-3 Type Test Verification Report

Inverter Model	GW8K-DT、GW10KT-DT、GW12KT-DT、GW15KT-DT、GW17KT-DT、GW20KT-DT、GW25KT-DT		
Manufacturer Reference number	2021033001		
Micro-generator technology	Grid-tied PV inverter		
Manufacturer name	Jiangsu GoodWe Power Supply Technology Co.,Ltd.		
Address	No.90 Zijin Rd., New District, Suzhou, 215011, China		
Tel	+86 512 6239 7998	**Fax**	+86 512 6239 7972
E:mail	service@goodwe.com	**Web site**	http://www.goodwe.com

Maximum rated capacity	**Connection Option**	
	8	**kW three phase**
	10	**kW three phase**
	12	**kW three phase**
	15	**kW three phase**
	17	**kW three phase**
	20	**kW three phase**
	25	**kW three phase**

Manufacturer Type Test declaration. - I certify that all products supplied by the company with the above Type Tested reference number will be manufactured and tested to ensure that they perform as stated in this document, prior to shipment to site and that no site modifications are required to ensure that the product meets all the requirements of EREC G99.

Signed	 Jing Xie	**On behalf of**	 Min Huang

The testing has been carried out by people with sufficient technical competency to carry out the tests.
1. Operating Range: Two tests should be carried with the Power Generating Module operating at Registered Capacity and connected to a suitable test supply or grid simulation set. The power supplied by the primary source shall be kept stable within ± 5 % of the apparent power value set for the entire duration of each test sequence. Frequency, voltage and Active Powermeasurements at the output terminals of the Power Generating Module shall be recorded every second. The tests will verify that the Power Generating Module can operate within the required ranges for the specified period of time. The Interface Protection shall be disabled during the tests. In case of a PV Power Park Module the PV primary source may be replaced by a DC source. In case of a full converter Power Park Module (eg wind) the primary source and the prime mover Inverter/rectifier may be replaced by a DC source.

Test 1 Voltage = 85% of nominal (195.5 V) Frequency = 47 Hz Power factor = 1 Period of test 20s	Result Pass
Test 2 Voltage = 85% of nominal (195.5 V) Frequency = 47.5 Hz Power factor = 1 Period of test 90 minutes	Result Pass
Test 3 Voltage = 110% of nominal (253 V). Frequency = 51.5 Hz Power factor = 1 Period of test 90 minutes	Result Pass
Test 4 Voltage = 110% of nominal (253 V). Frequency = 52.0 Hz Power factor = 1 Period of test 15 minutes	Result Pass

2. **Power Quality – Harmonics:** For Power Generating Modules of Registered Capacity of less than 75 A per phase (ie 50 kW) the test requirements are specified in Annex A.7.1.5. These tests should be carried out as specified in BS EN 61000-3-12 The results need to comply with the limits of Table 2 of BS EN 61000-3-12 for single phase equipment and Table 3 of BS EN 610000-3-12 for three phase equipment. Power Generating Modules with emissions close to the limits laid down in BS EN 61000-3-12 may require the installation of a transformer between 2 and 4 times the rating of the Power Generating Module in order to accept the connection to a Distribution Network. For Power Generating Modules of Registered Capacity of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC G5.	**Pass**

Power Generating Module tested to BS EN 61000-3-12		
Micro-generator rating per phase (rpp)	8.3kVA	Harmonic % = Measured

			Value (A) x 23/rating per phase (kVA)

	100% of rated output						Limit in BS EN 61000-3-12	
	L1		L2		L3			
	Measured Value (A)	%	Measured Value (A)	%	Measured Value (A)	%	1 phase	3 phase
2	0.169	0.46%	0.455	1.25%	0.353	0.98%	8%	8%
3	0.067	0.18%	0.118	0.32%	0.069	0.19%	21.6%	---
4	0.151	0.41%	0.136	0.37%	0.100	0.27%	4%	4%
5	0.312	0.85%	0.262	0.72%	0.258	0.72%	10.7%	10.7%
6	0.023	0.06%	0.045	0.12%	0.037	0.10%	2.67%	2.67%
7	0.22	0.61%	0.191	0.52%	0.227	0.63%	7.2%	7.2%
8	0.091	0.25%	0.105	0.29%	0.082	0.23%	2%	2%
9	0.021	0.05%	0.032	0.08%	0.030	0.08%	3.8%	---
10	0.022	0.06%	0.031	0.08%	0.034	0.09%	1.6%	1.6%
11	0.147	0.40%	0.168	0.46%	0.161	0.44%	3.1%	3.1%
12	0.022	0.06%	0.038	0.10%	0.030	0.08%	1.33%	1.33%
13	0.141	0.38%	0.151	0.41%	0.139	0.38%	2%	2%
THD	0.547	1.50%	0.676	1.86%	0.598	1.67%	23%	13%
PWHD	0.653	2.25%	0.655	2.26%	0.737	2.54%	23%	22%
	At 45-55% of rated output						Limit in BS EN 61000-3-12	
	L1		L2		L3			
	Measured Value (A)	%	Measured Value (A)	%	Measured Value (A)	%	1 phase	3 phase
2	0.125	0.68%	0.278	1.53%	0.194	1.08%	8%	8%
3	0.049	0.26%	0.121	0.66%	0.045	0.25%	21.6%	---
4	0.130	0.71%	0.142	0.78%	0.119	0.66%	4%	4%
5	0.270	1.47%	0.232	1.27%	0.224	1.25%	10.7%	10.7%
6	0.016	0.08%	0.030	0.16%	0.028	0.16%	2.67%	2.67%
7	0.173	0.94%	0.157	0.86%	0.175	0.98%	7.2%	7.2%
8	0.066	0.36%	0.071	0.39%	0.061	0.34%	2%	2%
9	0.016	0.08%	0.025	0.13%	0.025	0.14%	3.8%	---
10	0.015	0.08%	0.022	0.12%	0.019	0.10%	1.6%	1.6%
11	0.090	0.49%	0.114	0.69%	0.116	0.65%	3.1%	3.1%
12	0.023	0.12%	0.025	0.14%	0.028	0.16%	1.33%	1.33%
13	0.086	0.47%	0.087	0.48%	0.082	0.46%	2%	2%
THD	0.476	2.26%	0.485	2.67%	0.420	2.35%	23%	13%
PWHD	0.670	2.31%	0.705	2.43%	0.748	2.58%	23%	22%

3. Power Quality – Voltage fluctuations and Flicker: For Power Generating Modules of Registered Capacity of less than 75 A per phase (ie 50 kW) these tests should be undertaken in accordance with Annex A.7.1.4.3. Results should be normalised to a standard source impedance, or if this results in figures above the limits set in BS EN 61000-3-11 to a suitable Maximum Impedance. For Power Generating Modules of Registered Capacity of greater than 75 A per phase (ie 50 kW) the installation must be designed in accordance with EREC P28.	**Pass**

	Starting			**Stopping**			**Running**	
	d_{max}	d_c	$d_{(t)}$	d_{max}	d_c	$d_{(t)}$	Pst	Plt 2 hours
Measured Values at test impedance	0.05%	0.51%	0	0.04%	0.52%	0	0.16	0.15
Normalised to standard impedance	0.05%	0.04%	0	0.26%	0.06%	0	0.08	0.08
Normalised to required maximum impedance	0.06%	0.04%	0	0.28%	0.07%	0	0.10	0.08
Limits set under BS EN 61000-3-2	4%	3.3%	3.3%	4%	3.3%	3.3%	1.0	6.5

Test Impedance	R	0.24	Ω	X	0.15	Ω
Standard Impedance	R	0.24 * 0.4 ^	Ω	X	0.15 * 0.25 ^	Ω
Maximum Impedance	R	0.25	Ω	X	0.16	Ω

Applies to three phase and split single phase Micro-generators.

^ Applies to single phase Micro-generators and Micro-generators using two phases on a three phase system.

For voltage change and flicker measurements the following formula is to be used to convert the measured values to the normalised values where the power factor of the generation output is 0.98 or above.

Normalised value = Measured value × reference source resistance/measured source resistance at test point × 3.68/rating per phase.

Single phase units reference source resistance is 0.4 Ω

Two phase units in a three phase system reference source resistance is 0.4 Ω.

Two phase units in a split phase system reference source resistance is 0.24 Ω.

Three phase units reference source resistance is 0.24 Ω.

Where the power factor of the output is under 0.98 then the X to R ratio of the test impedance should be close to that of the Standard Impedance.

The stopping test should be a trip from full load operation.

The duration of these tests need to conform to the particular requirements set out in the testing notes for the technology under test. Dates and location of the test need to be noted below.

Test start date	2020.11.12	Test end date	2020.12.18
Test location	Jiangsu GoodWe lab		

4. Power quality. DC injection This test should be carried out in accordance with EN 50438 Annex D.3.10			**Pass**
Test level power	10%	55%	100%
Recorded value in Amps	0.04	0.05	0.08
As % of rated AC current	0.11%	0.14%	0.22%
Limit	0.25%	0.25%	0.25%

5. Power Quality. Power factor This test shall be carried out in accordance with EN 50538 Annex D.3.4.1 but with nominal voltage -6% and +10%. Voltage to be maintained within ±1.5% of the stated level during the test.			**Pass**
	216.2 V	230 V	253 V
20% of Registered Capacity	0.9978	0.9979	0.9982
50% of Registered Capacity	0.9989	0.9990	0.9992
75% of Registered Capacity	0.9992	0.9993	0.9993
100% of Registered Capacity	0.9993	0.9994	0.9994
Limit	>0.95	>0.95	>0.95

6. Protection. Frequency test These tests should be carried out in accordance with the Annex A.7.1.2.3.					**Pass**	
Function	Setting		Trip test		No trip test	
	Frequency	Time delay	Frequency	Time delay	Frequency / time	Confirm no trip
U/F stage 1	47.5 Hz	20 s	47.45 Hz	20.33 s	47.7Hz / 25s	no trip
U/F stage 2	47 Hz	0.5 s	46.62 Hz	0.71 s	47.2Hz / 19.98s	no trip
					46.8Hz / 0.48s	no trip
O/F stage 1	52 Hz	0.5 s	52.06 Hz	0.64 s	51.8Hz / 89.98s	no trip
					52.2Hz / 0.48s	no trip
Note. For frequency trip tests the frequency required to trip is the setting ± 0.1 Hz. In order to measure the time delay a larger deviation than the minimum required to operate the projection can be used. The “No trip tests” need to be carried out at the setting ± 0.2 Hz and for the relevant times as shown in the table above to ensure that the protection will not trip in error.						

7. Protection. Voltage test These tests should be carried out in accordance with Annex A.7.1.2.2	**Pass**
Type A LV	
Ph1	

Function	Setting		Trip test		No trip test	
	Voltage	Time delay	Voltage	Time delay	Voltage / time	Confirm no trip
U/V stage 1	184 V	2.5 s	183.25 V	2.53s	188V / 3.5s	no trip
					180V / 2.48s	no trip
O/V stage 1	262.2 V	1.0 s	262.5 V	1.27 s	258.2V 2.0s	no trip
O/V stage 2	273.7 V	0.5 s	273.1 V	0.66 s	269.7V 0.98s	no trip
					277.7V 0.48s	no trip
Ph2						
Function	Setting		Trip test		No trip test	
	Voltage	Time delay	Voltage	Time delay	Voltage / time	Confirm no trip
U/V stage 1	184 V	2.5 s	183.28 V	2.54 s	188V / 3.5s	no trip
					180V / 2.48s	no trip
O/V stage 1	262.2 V	1.0 s	262.6 V	1.23 s	258.2V 2.0s	no trip
O/V stage 2	273.7 V	0.5 s	273.9 V	0.62 s	269.7V 0.98s	no trip
					277.7V 0.48s	no trip
Ph3						
Function	Setting		Trip test		No trip test	
	Voltage	Time delay	Voltage	Time delay	Voltage / time	Confirm no trip
U/V stage 1	184 V	2.5 s	183.47 V	2.63 s	188V / 3.5s	no trip
					180V / 2.48s	no trip
O/V stage 1	262.2 V	1.0 s	262.3 V	1.12 s	258.2V 2.0s	no trip
O/V stage 2	273.7 V	0.5 s	273.9 V	0.61 s	269.7V 0.98s	no trip
					277.7V 0.48s	no trip

Protection. Loss of Mains test

For PV Inverters shall be tested in accordance with BS EN 62116. Other Inverters should be tested in accordance with EN 50438 Annex D.2.5 at 10%, 55% and 100% of rated power.						
For **Inverters** tested to BS EN 62116 the following sub set of tests should be recorded in the following table.						
Test Power	33%	66%	100%	33%	66%	100%
Balancing load on islanded network	-5% Q Test 22	-5% Q Test 12	-5% P Test 5	+5% Q Test 31	+5% Q Test 21	+5% P Test 10
Trip time. Limit is 0.5s	0.133	0.164	0.464	0.115	0.208	0.482
For other Inverters should be tested in accordance with EN 50438 Annex D.2.5 at 10%, 55% and 100% of rated power. the following sub set of tests should be recorded in the following table For Multi phase Micro-generators confirm that the device shuts down correctly after the removal of a single fuse as well as operation of all phases.						
To be carried out at three output power levels with a tolerance of plus or minus 5% in Test Power levels.						
Test Power	10%	55%	100%	10%	55%	100%
Balancing load on islanded network	95% of Registered Capacity	95% of Registered Capacity	95% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity	105% of Registered Capacity
Trip time. Ph1	NA	NA	NA	NA	NA	NA
Trip time. Ph2	NA	NA	NA	NA	NA	NA
Trip time. Ph3	NA	NA	NA	NA	NA	NA
Note for technologies which have a substantial shut down time this can be added to the 0.5 s in establishing that the trip occurred in less than 0.5 s. Maximum shut down time could therefore be up to 1.0 s for these technologies						

Protection – Frequency change, Vector Shift Stability test: This test should be carried out in accordance with EREC G98 Annex A1 A.1.2.6 (Inverter connected) or Annex A2 A.2.2.6 (Synchronous).			**Pass**
	Start Frequency	Change	Confirm no trip
Positive Vector Shift	49Hz	+50 degrees	no trip
Negative Vector Shift	50.5Hz	- 50degrees	no trip

Protection – Frequency change, RoCoF Stability test: The requirement is specified in section 11.3, test procedure in Annex A.1.2.6 (Inverter connected) or Annex A2 A.2.2.6 (Synchronous).			**Pass**
Ramp range	Test frequency ramp:	Test Duration	Confirm no trip
49.0 Hz to 51.0 Hz	+0.95Hz/sec	2.1 s	no trip
51.0 Hz to 49.0 Hz	-0.95Hz/sec	2.1 s	no trip

9. Limited Frequency Sensitive Mode – Overfrequency test: This test should be carried out in accordance with EN 50438 Annex D.3.3 Power response to over- frequency. The test should be carried out using the specific threshold frequency of 50.4 Hz and **Droop** of 10%. Above 50.9Hz ,the slope should be above 0.5%Pmax/s.				**Pass**
Test sequence at Registered Capacity >80%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	25076	50	25770	
Step b) 50.45 Hz ±0.05 Hz	24601	50.45	25263	-5.263%
Step c) 50.70 Hz ±0.10 Hz	22127	50.7	22701	-5.086%
Step d) 51.15 Hz ±0.05 Hz	17628	51.15	18059	-5.035%
Step e) 50.70 Hz ±0.10 Hz	21819	50.7	22423	-4.605%
Step f) 50.45 Hz ±0.05 Hz	24553	50.45	25271	-4.78%
Step g) 50.00 Hz ±0.01 Hz	25029	50	25670	
Above 50.9Hz slope				40.28%
Test sequence at Registered Capacity 40% - 60%	Measured Active Power Output	Frequency	Primary Power Source	Active Power Gradient
Step a) 50.00 Hz ±0.01 Hz	12547	50	12815	
Step b) 50.45 Hz ±0.05 Hz	12308	50.45	12601	-10.46%
Step c) 50.70 Hz ±0.10 Hz	11071	50.7	11336	-10.163%
Step d) 51.15 Hz ±0.05 Hz	8861	51.15	9075	-10.174%
Step e) 50.70 Hz ±0.10 Hz	11026	50.7	11287	-9.862%
Step f) 50.45 Hz ±0.05 Hz	12289	50.45	12579	-9.69%
Step g) 50.00 Hz ±0.01 Hz	12531	50	12801	
Above 50.9Hz slope				81.392%

10. Protection. Re-connection time				**Pass**
Test should prove that the reconnection sequence starts after a minimum delay of 20 s for restoration of voltage and frequency to within the stage 1 settings of Table 10.1.				
Time delay setting				
Measured delay time(s)	At 258.2V	At 204.1V	At 47.6Hz	At 51.9Hz
	46.8s	46.2s	46.4s	46.6s
Confirmation that the SSEG does not re-connect	At 266.2V	At 196.1V	At 47.4Hz	At 52.1Hz
	no reconnection	no reconnection	no reconnection	no reconnection

11. Fault level contribution: These tests shall be carried out in accordance with EREC G98 Annex A1 A.1.3.5 (Inverter connected) and Annex A2 A.2.3.4 (Synchronous).		**Pass**
For Inverter output		
Time after fault	Volts	Amps
20ms	221V	331mA
100ms	220V	300mA
250ms	197.2V	320mA
500ms	195.4V	340mA

Time to trip	/	/

12. Self-Monitoring solid state switching: No specified test requirements. Refer to Annex A.7.1.7.	
It has been verified that in the event of the solid state switching device failing to disconnect the **Power Park Module**, the voltage on the output side of the switching device is reduced to a value below 50 volts within 0.5 s.	N/A

13. Wiring functional tests: If required by para 15.2.1.	
Confirm that the relevant test schedule is attached (tests to be undertaken at time of commissioning)	N/A

14. Logic Interface (input port).	
Confirm that an input port is provided and can be used to shut down the module.	Pass

Additional comments
GW8K-DT、GW10KT-DT、GW12KT-DT、GW15KT-DT、GW17KT-DT、GW20KT-DT is similar to GW25KT-DT in circuit and construction except for output rating of current and power. The test result can refer to GW25KT-DT .